## Computer Engineering Areas of Study

1. VLSI – VLSI and Architecture
2. ECS – Embedded Control Systems
3. CN – Communications and Networks
4. DDSS – Distributed, Dependable Secure Systems
5. MSP – Multimedia and Signal Processing
6. SO – Systems Optimization

M* - Content of course is Master level
D* - Content of course is Doctorate level

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<thead>
<tr>
<th>Course &amp; Prefix</th>
<th>Course Title (Credit Hours)</th>
<th>VLSI</th>
<th>ECS</th>
<th>CN</th>
<th>DDSS</th>
<th>MSP</th>
<th>SO</th>
</tr>
</thead>
<tbody>
<tr>
<td>APM 506</td>
<td>Computational methods (3)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>X</td>
</tr>
<tr>
<td>APM 523</td>
<td>Optimization (Continuous) (D*) (3)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>X</td>
</tr>
<tr>
<td>CSE 408/598</td>
<td>Multimedia Information Systems (3)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>X</td>
</tr>
<tr>
<td>CSE 412/598</td>
<td>Database Management (3)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>X</td>
</tr>
<tr>
<td>CSE 420/598</td>
<td>Computer Architecture I (M*) (3)</td>
<td>X</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>CSE 430</td>
<td>Operating Systems (M*) (3)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>X</td>
</tr>
<tr>
<td>CSE 434/598</td>
<td>Computer Networks, or (M*) (3)</td>
<td>X</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>CSE 440/598</td>
<td>Compiler Construction I (M*) (3)</td>
<td>X</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>CSE 445/598</td>
<td>Distributed Software Development (3)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>X</td>
</tr>
<tr>
<td>CSE 450/598</td>
<td>Design and Analysis of Algorithms (M*) (3)</td>
<td>X</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>CSE 468/598</td>
<td>Computer Network Security (3)</td>
<td>X</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>CSE 509</td>
<td>Digital Video Processing (D*) (3)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>X</td>
<td></td>
</tr>
<tr>
<td>CSE 512</td>
<td>Distributed Database Systems (3)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>X</td>
<td></td>
</tr>
<tr>
<td>CSE 515</td>
<td>Multimedia Web Databases (3)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>X</td>
</tr>
<tr>
<td>CSE 520</td>
<td>Computer Architecture II (D*) (3)</td>
<td>X</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>CSE 522</td>
<td>Real Time Embedded Systems (D*) (3)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>X</td>
<td></td>
</tr>
<tr>
<td>CSE 531</td>
<td>Distributed &amp; Multiprocessor Operating Systems (D*) (3)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>X</td>
<td></td>
</tr>
</tbody>
</table>

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<table>
<thead>
<tr>
<th>Course &amp; Prefix</th>
<th>Course Title (Credit Hours)</th>
<th>VLSI</th>
<th>ECS</th>
<th>CN</th>
<th>DDSS</th>
<th>MSP</th>
<th>SO</th>
</tr>
</thead>
<tbody>
<tr>
<td>CSE 534</td>
<td>Advanced Computer Networks (D*) (3)</td>
<td></td>
<td></td>
<td>X</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>CSE 535</td>
<td>Mobile Computing (3)</td>
<td></td>
<td></td>
<td>X</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>CSE 536</td>
<td>Advanced Operating Systems (D*) (3)</td>
<td></td>
<td></td>
<td>X</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>CSE 539</td>
<td>Applied Cryptography (3)</td>
<td></td>
<td></td>
<td>X</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>CSE 543</td>
<td>Information Assurance and Security (3)</td>
<td></td>
<td></td>
<td>X</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>CSE 545</td>
<td>Software Security (3)</td>
<td></td>
<td></td>
<td>X</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>CSE 550</td>
<td>Combinatorial algorithms and intractability (M*) (3)</td>
<td></td>
<td></td>
<td>X</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>CSE 591/551</td>
<td>Foundations of Algorithms (M*) (3)</td>
<td></td>
<td></td>
<td>X</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>CSE 552</td>
<td>Randomized and Approximation Algorithms (3)</td>
<td></td>
<td></td>
<td>X</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>CSE 555</td>
<td>Theory of Computation (3)</td>
<td></td>
<td></td>
<td>X</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>CSE 565</td>
<td>Software Verification, Validation, and Testing (3)</td>
<td></td>
<td></td>
<td>X</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>CSE 572</td>
<td>Data Mining (3)</td>
<td></td>
<td></td>
<td>X</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>CSE 574</td>
<td>Planning and Learning Methods in AI</td>
<td></td>
<td></td>
<td>X</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>CSE 575</td>
<td>Statistical Machine Learning (3)</td>
<td></td>
<td></td>
<td>X</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>EEE 404/591</td>
<td>Real-Time Digital Signal Processing (M*) (4)</td>
<td></td>
<td></td>
<td>X</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>EEE 407/591</td>
<td>Digital Signal Processing (M*) (4)</td>
<td></td>
<td></td>
<td>X</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>EEE 425/591</td>
<td>Digital Circuits and Systems (M*) (4)</td>
<td></td>
<td></td>
<td>X</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>EEE 455/591</td>
<td>Communication Systems (M*) (3)</td>
<td></td>
<td></td>
<td>X</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>EEE 459/591</td>
<td>Communication Networks (M*) (3)</td>
<td></td>
<td></td>
<td>X</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Course &amp; Prefix</td>
<td>Course Title (Credit Hours)</td>
<td>VLSI</td>
<td>ECS</td>
<td>CN</td>
<td>DDSS</td>
<td>MSP</td>
<td>SO</td>
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<td>----</td>
</tr>
<tr>
<td>EEE 480/591</td>
<td>Feedback Systems (M*) (4)</td>
<td></td>
<td>X</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>EEE 481/591</td>
<td>Computer Controlled Systems (M*) (3)</td>
<td></td>
<td>X</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>EEE 505</td>
<td>Time-Frequency Signal Processing (3)</td>
<td></td>
<td></td>
<td>X</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>EEE 507</td>
<td>Multidimensional Signal Processing (3)</td>
<td></td>
<td>X</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>EEE 508</td>
<td>Digital Image and Video Processing and Compression (D*) (4)</td>
<td></td>
<td></td>
<td>X</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>EEE 509</td>
<td>DSP Algorithms and Software (3)</td>
<td></td>
<td></td>
<td>X</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>EEE 511</td>
<td>Artificial Neural Computation (3)</td>
<td></td>
<td></td>
<td>X</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>EEE 525</td>
<td>VLSI Design (D*) (4)</td>
<td></td>
<td>X</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>EEE 526</td>
<td>VLSI Architectures (3)</td>
<td></td>
<td>X</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>EEE 551</td>
<td>Information Theory (D*) (3)</td>
<td></td>
<td></td>
<td>X</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>EEE 552</td>
<td>Digital Communications (3)</td>
<td></td>
<td>X</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>EEE 553</td>
<td>Coding and Cryptography (3)</td>
<td></td>
<td></td>
<td>X</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>EEE 554</td>
<td>Random Signal Theory (D*) (3)</td>
<td></td>
<td></td>
<td>X</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>EEE 555</td>
<td>Modeling and Performance Analysis (3)</td>
<td></td>
<td></td>
<td>X</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>EEE 557</td>
<td>Broadband Networks (3)</td>
<td></td>
<td></td>
<td>X</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>EEE 558</td>
<td>Wireless Communications (3)</td>
<td></td>
<td></td>
<td>X</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>EEE 582</td>
<td>Linear System Theory</td>
<td></td>
<td></td>
<td>X</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>EEE 585</td>
<td>Digital Control Systems (D*) (3)</td>
<td></td>
<td></td>
<td>X</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>EEE 586</td>
<td>Nonlinear Control Systems (3)</td>
<td></td>
<td></td>
<td>X</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>EEE 587</td>
<td>Optimal Control (3)</td>
<td></td>
<td></td>
<td>X</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Course &amp; Prefix</td>
<td>Course Title (Credit Hours)</td>
<td>VLSI</td>
<td>ECS</td>
<td>CN</td>
<td>DDSS</td>
<td>MSP</td>
<td>SO</td>
</tr>
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<td>------</td>
<td>-----</td>
<td>----</td>
</tr>
<tr>
<td>EEE 588</td>
<td>Design of Multivariable Control Systems (3)</td>
<td>X</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>EEE 606</td>
<td>Adaptive Signal Processing</td>
<td></td>
<td>X</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>EEE 607</td>
<td>Speech Coding for Multimedia Communications</td>
<td></td>
<td></td>
<td>X</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>EEE 625</td>
<td>Advanced VLSI Design (4)</td>
<td>X</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>EEE 686</td>
<td>Adaptive Control (3)</td>
<td></td>
<td></td>
<td>X</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>IEE 533</td>
<td>Scheduling (3)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>X</td>
<td></td>
</tr>
<tr>
<td>IEE 572</td>
<td>Design of Engineering Experiments (3)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>X</td>
</tr>
<tr>
<td>IEE 574</td>
<td>Applied Deterministic Operations Res. Methods (3)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>X</td>
</tr>
<tr>
<td>IEE 575</td>
<td>Applied Stochastic Operations Res. Methods (3)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>X</td>
</tr>
<tr>
<td>IEE 620</td>
<td>Optimization I (Discrete) (D*) (3)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>X</td>
</tr>
<tr>
<td>IEE 670</td>
<td>Mathematical Statistics (3)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>X</td>
</tr>
</tbody>
</table>

**Course Descriptions**
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